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APPLICANTS

James W. Adkisson, Jericho, VT;

Greg Bazan, Essex Junction, VT;

John M. Cohn, Richmond, VT; Francis Gravel, Westford, VT;

Leendert M. Huisman, South Burlington, VT;

Phillip J. Nigh, Williston, VT;

Leah M.P. Pastel, Essex, VT;

Kenneth Rowe, Burlington, VT;

Thomas G. Sopchak, Williston, VT;

David E. Sweenor, South Burlington, VT;

** CONTINUING DATA *****

None TT

** FOREIGN APPLICATIONS *****

None TT

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 09/10/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY VT	SHEETS DRAWING 3	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged Examiner's Signature <i>T. J. W.</i> Initials TT				

ADDRESS

30449

SCHMEISER, OLSEN + WATTS

3 LEAR JET LANE

SUITE 201

LATHAM, NY

12110

TITLE

DEFECT DIAGNOSIS FOR SEMICONDUCTOR INTEGRATED CIRCUITS

☐ All Fees☐ 1.16 Fees (Filing)